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Behzad Razavi received the B.Sc. degree in electrical engineering from Sharif University of Technology in 1985, and the M.Sc. and Ph.D. degrees in electrical engineering from Stanford University in 1988 and 1992, respectively. He

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was with AT&T Bell Laboratories and subsequently Hewlett-Packard Laboratories until 1996. He was also an Adjunct Professor at Princeton University from 1992 to 1994.

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(a) Calculate the drain current  $I_D$  of the transistor in the amplifier (neglect channel length modulation). Verify

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your result by SPICE (use the .op analysis).  
(b) Calculate the small-signal gain  $v_{out}/v_{in}$  by hand calculation. Using the .tran analysis in SPICE to verify your result, assuming  $C_1$ ,  $C_2$ , and  $C_S$  are infinite (using  $C = 1 \text{ F}$  in simulation).

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5P 6E 6P 7E 7P 8E 8P  
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45. In the limiting  
circuit of Fig. 3.51, plot  
the currents flowing  
through and as a  
function of time if the  
input is given by and  
and. 46. Design the  
limiting circuit of Fig.  
3.51 for a negative  
threshold of  $V$  and a



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positive threshold of V.  
Assume the input peak  
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received his  
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and master's and  
doctorate degrees from  
Stanford University in

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1988 and 1992. He worked for AT&T Bell Laboratories and Hewlett-Packard Laboratories until 1996, and since has been an associate professor and a professor of electrical engineering at UCLA.

## **Behzad Razavi**

Iout1 Iout2 V1 M2 M1  
V2 P CGD3 M0 X M3

Figure 19.69

Differential pair  
operating as a current

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switch Solution Recall from Chapter that for the differential pair to experience complete switching, the differential swing  $|V_2 - V_1|_{\sqrt{}}$  must exceed  $2(V_{GS} - V_{TH})_{eq}$ , where  $(V_{GS} - V_{TH})_{eq}$  is the overdrive of M1 and M2 in equilibrium, i.e ...

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